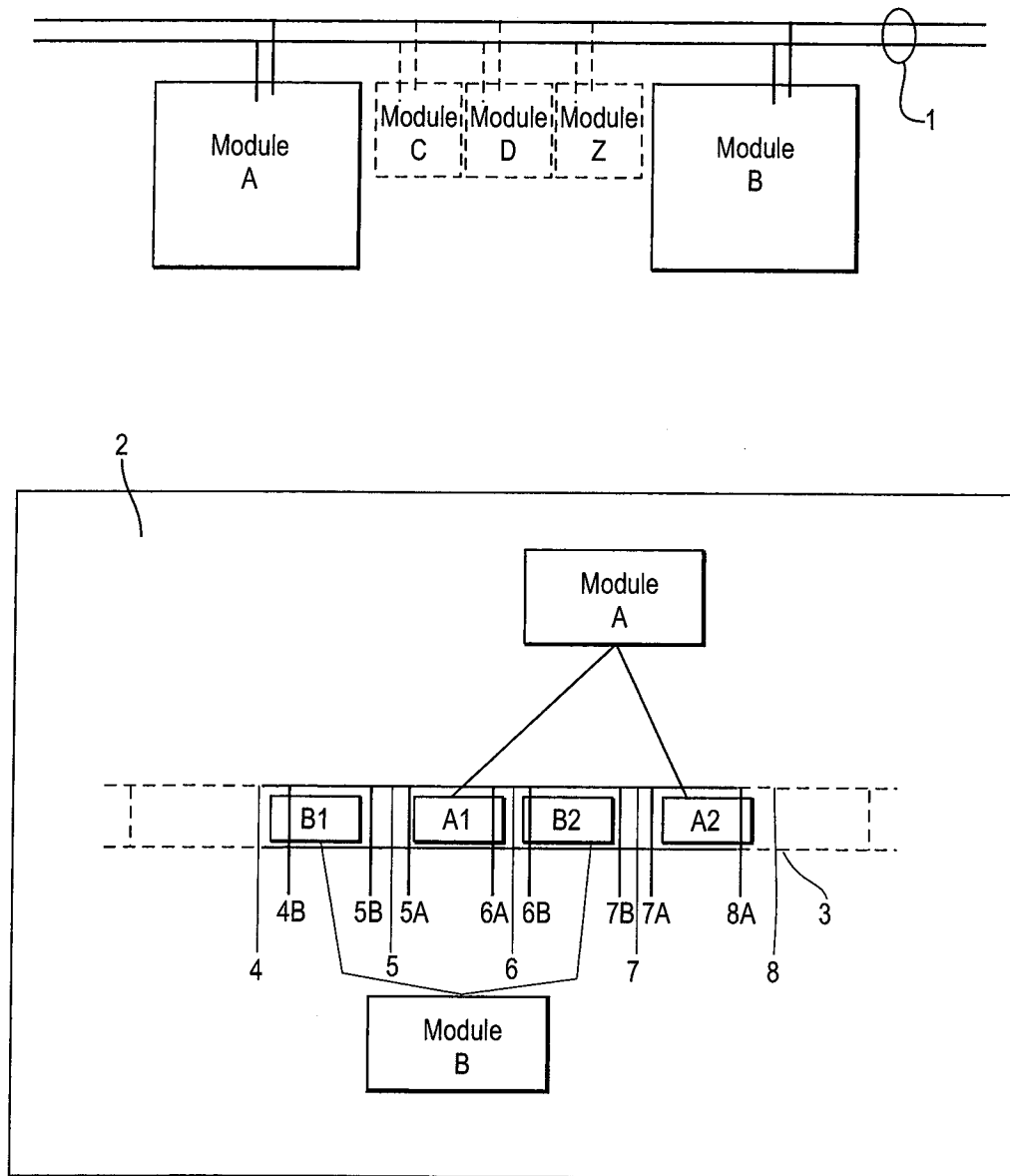


1/8

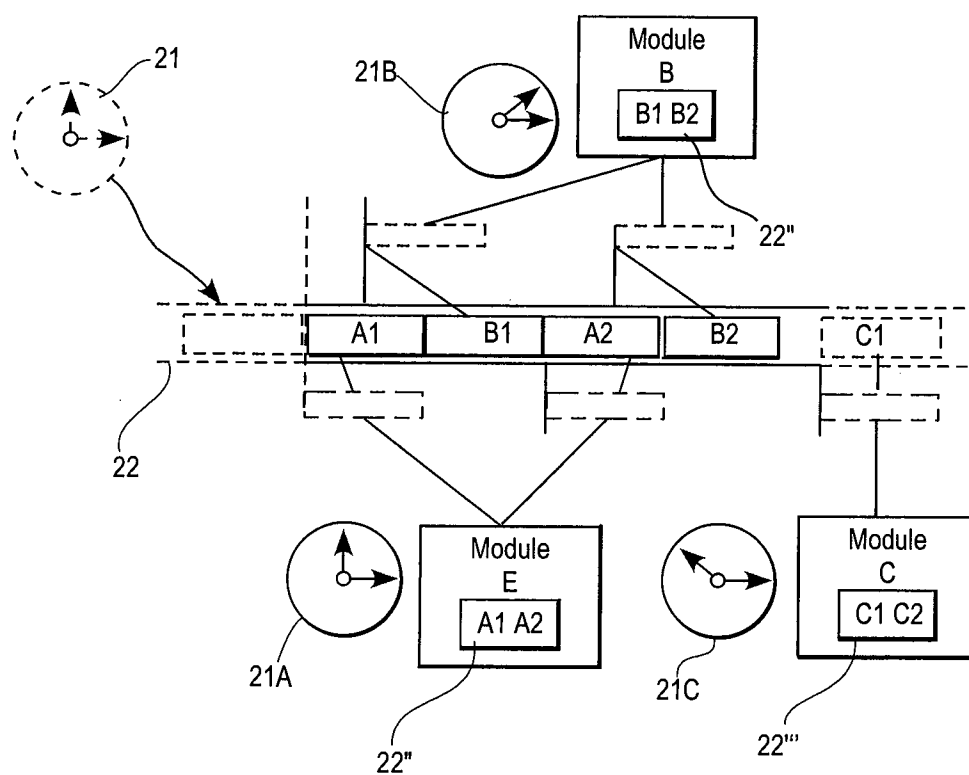
FIG. 1



LEGEND
A1, A2, B1, B2=Messages

2/8

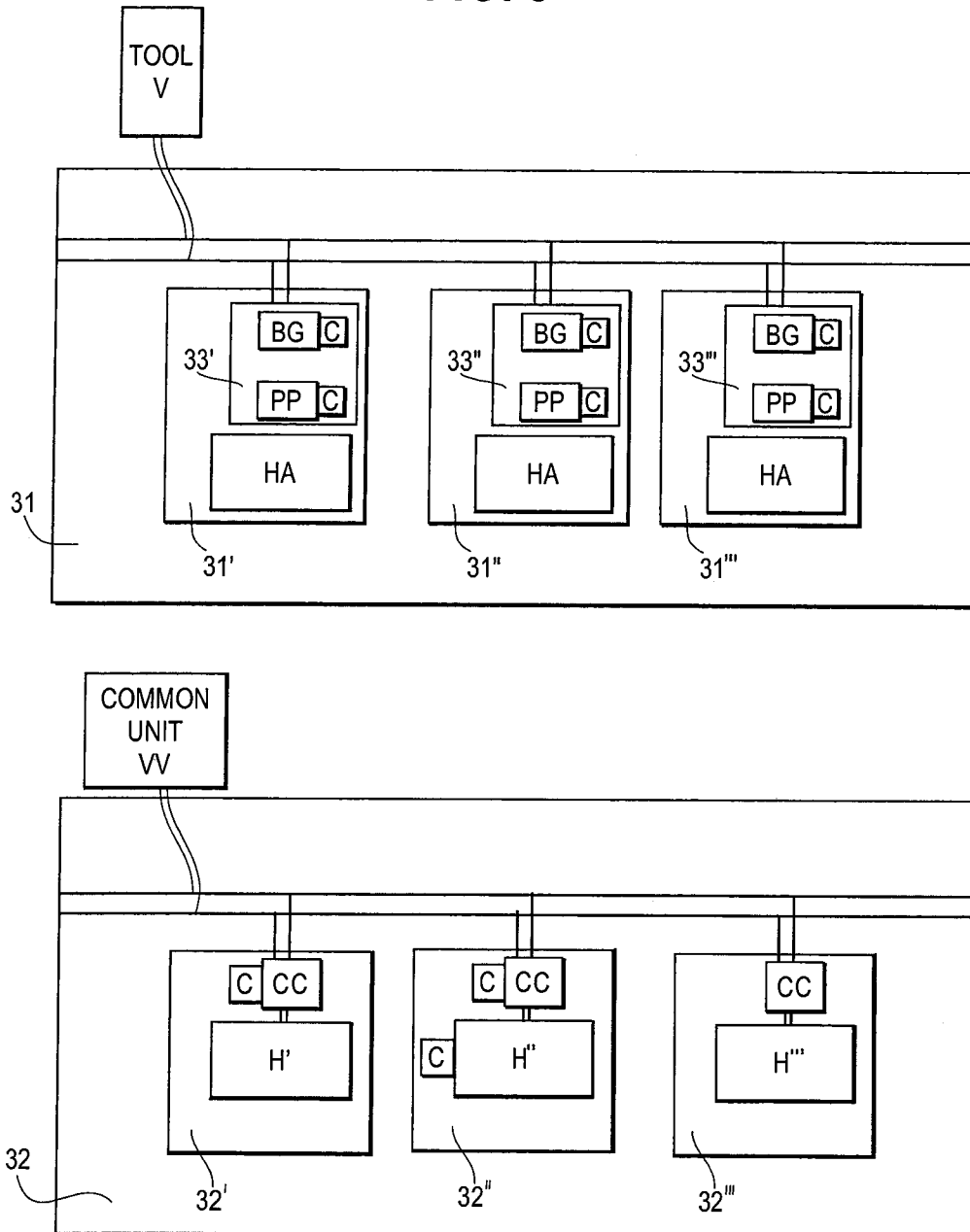
FIG. 2



LEGEND
A1, A2, B1, B2, C1, C2=Messages

3/8

FIG. 3

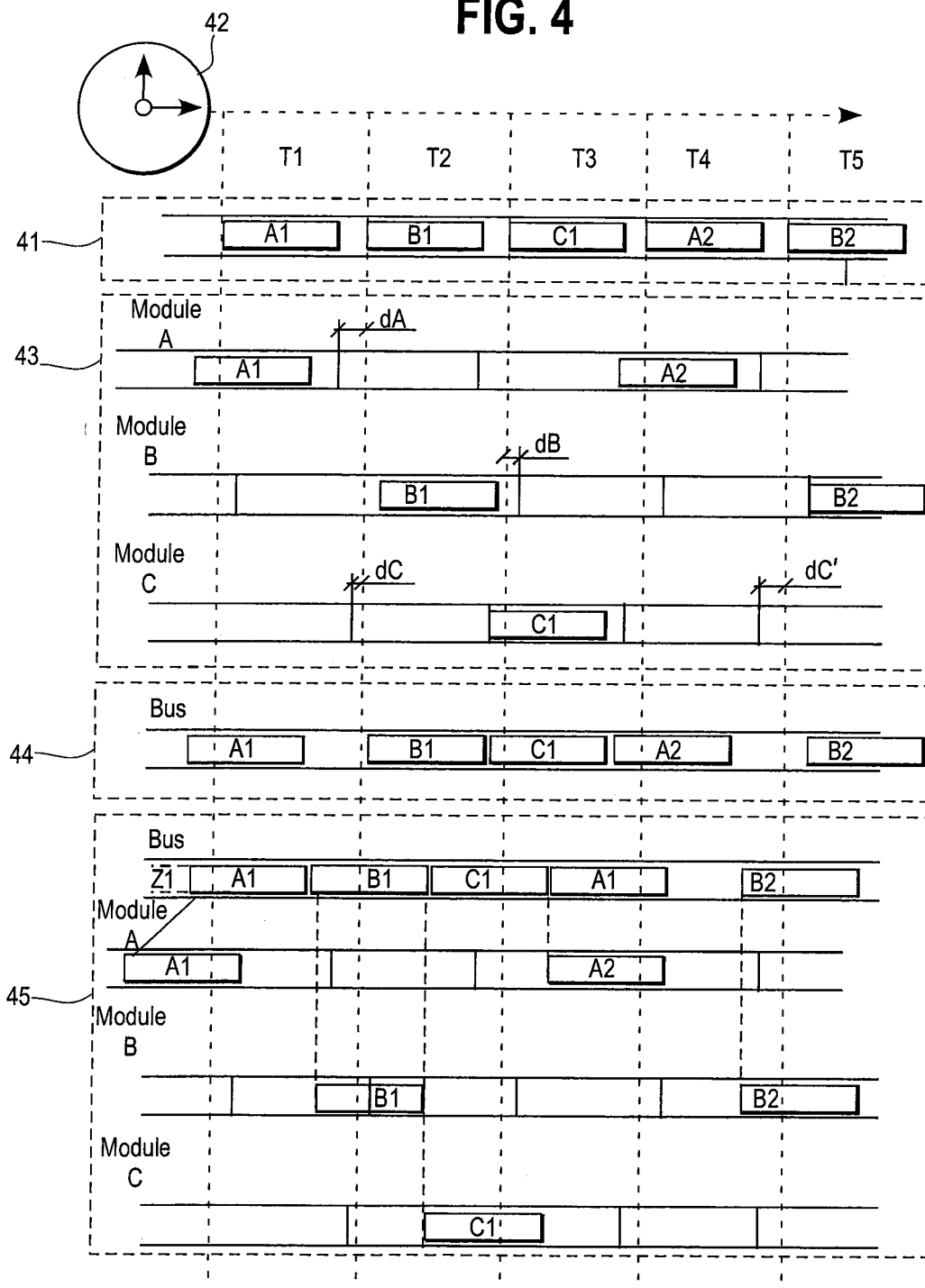


LEGEND

BG=BUS Guardian
 PP=Protocol Processor
 HA=Requisite Hardware
 C=Clock
 CC=Can Controller
 H', H'', H'''=Hardware

4/8

FIG. 4



LEGEND

T1, T2, T3, T4, T5=Time slot

A1, A2, B1, B2, C1, Z1 =Message

dA=Clock delta in module A

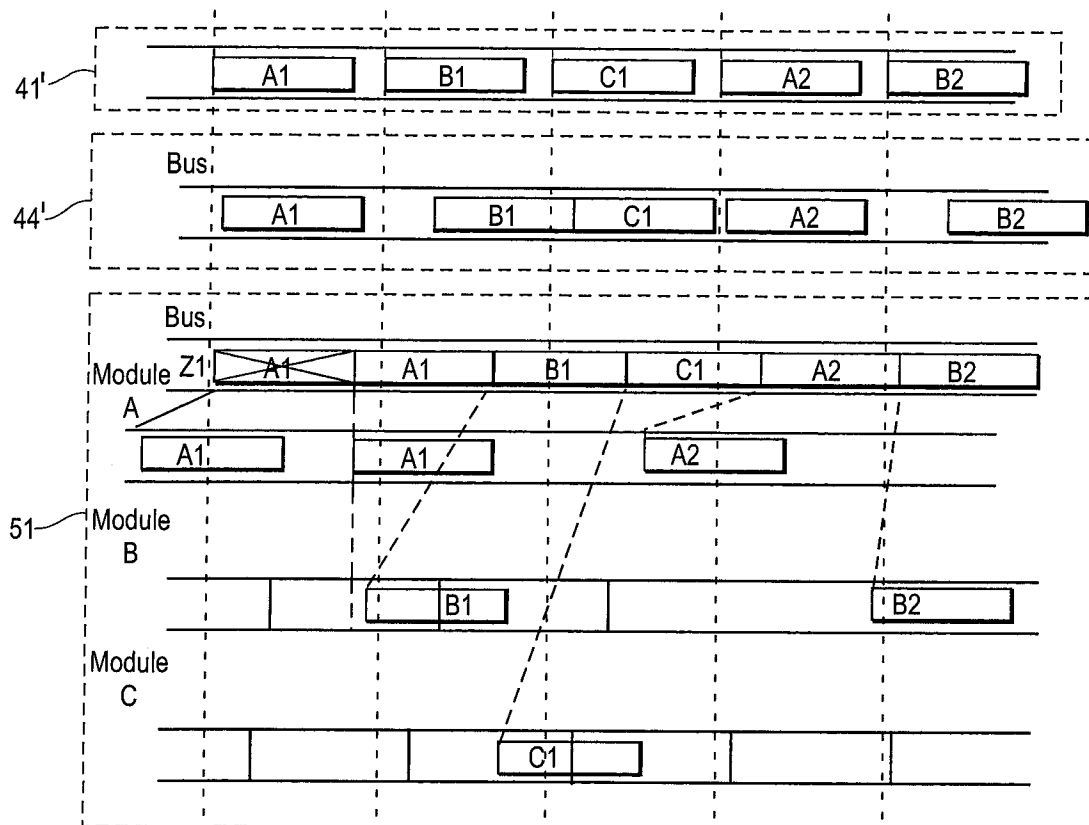
dB=Clock delta in module B

dC=Clock delta in module C

dC'=Increased clock delta in module C

5/8

FIG. 5

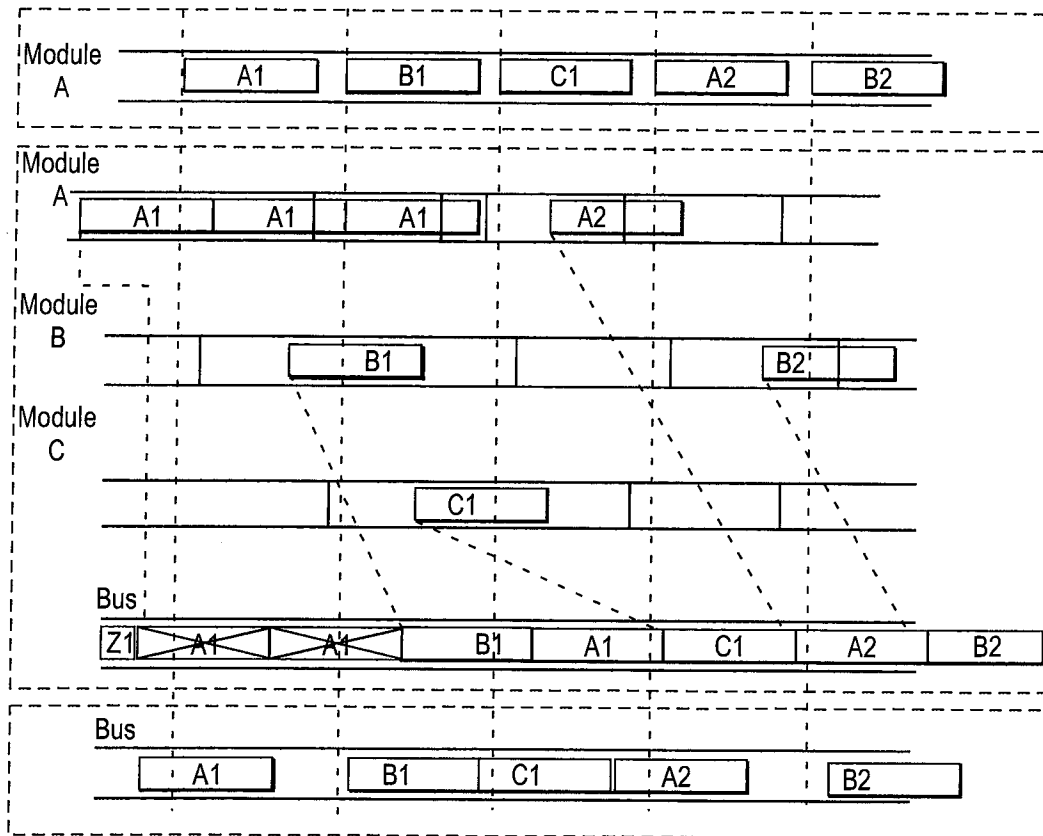


LEGEND

A1, A2, B1, B2, C1, Z1=Messages

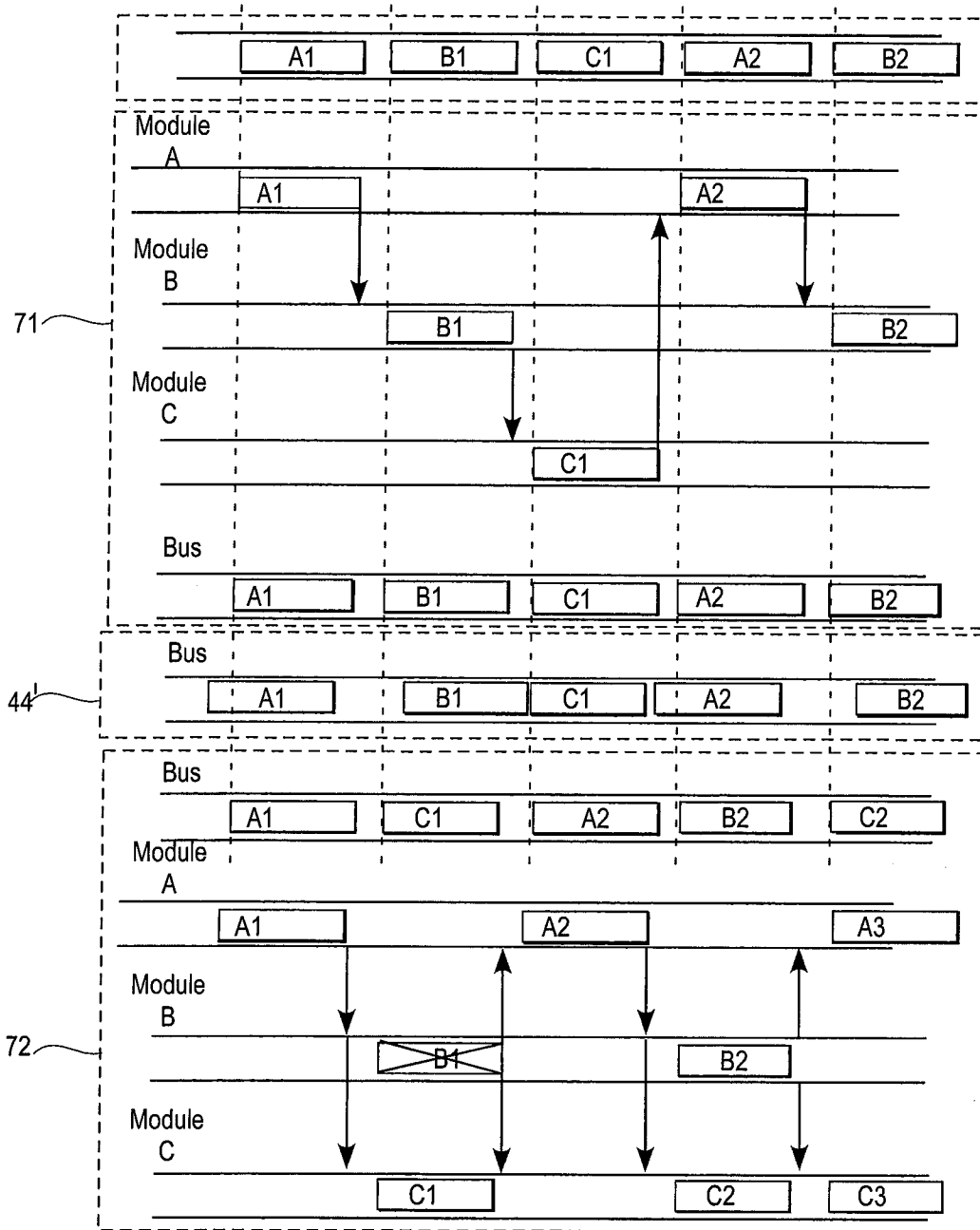
6/8

FIG. 6



7/8

FIG. 7



LEGEND

A1, A2, A3, B1, B2, C1, C2, C3=Messages

